



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kie Y. Ahn et al.

Title: METHOD FOR MAKING A FERROELECTRIC MEMORY TRANSISTOR

Docket No.: 303.716US2

Filed: June 20, 2003

Examiner: Tuan H. Nguyen

Customer No.: 21186

Serial No.: 10/600,965

Due Date: November 30, 2004

Group Art Unit: 2811

Confirmation No.: 6564

Commissioner for Patents

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number: 21186

By

Eduardo E. Drake

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Kyrstih Ryan
Name

Kyrstih Ryan
Signature

S/N 10/600,965

PATENT

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Title: METHOD FOR MAKING A FERROELECTRIC MEMORY TRANSISTOR

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
10/931409	August 31, 2004	303.716US3	STRUCTURES, METHODS, AND SYSTEMS FOR FERROELECTRIC MEMORY TRANSISTORS

Respectfully submitted,

KIE Y. AHN ET AL.

By Applicants' Representatives,

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18 Nov. 2004

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Name

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